

AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions and listings of claims in the application.

1. **(Previously Presented)** An optical transceiver module comprising:

a transceiver housing including two sides, a top, a bottom, and front and rear faces, at least the front face having right and left sides, the transceiver housing defining a pair of optical ports within an interior of the transceiver housing, and the bottom of the transceiver housing defining a pair of optical port slots, each of the optical port slots being in communication with a respective optical port;

a transceiver substrate arranged so that the transceiver substrate is oriented substantially perpendicularly with respect to the top and bottom of the transceiver housing, the transceiver substrate including an electrical connector located proximate the bottom of the transceiver housing;

a receive optical assembly that defines a longitudinal axis, the receive optical assembly being positioned so that the longitudinal axis of the receive optical assembly is substantially perpendicular to the transceiver substrate and the receive optical subassembly being disposed proximate the left side of the front face of the transceiver housing; and

a transmit optical assembly that defines a longitudinal axis, the transmit optical assembly being positioned so that the longitudinal axis of the transmit optical assembly is substantially perpendicular to the transceiver substrate, and the transmit optical subassembly being disposed proximate the right side of the front face.

2. **(Previously Presented)** The optical transceiver module as recited in claim 1, wherein the axes respectively defined by the transmit optical assembly and the receive optical assembly are positioned above an imaginary horizontal plane that bisects the optical transceiver module.

3. **(Previously Presented)** The optical transceiver module as recited in claim 1, wherein the electrical connector of the substrate is configured to physically and electrically interface with a host bus adaptor.

4. **(Cancelled)**

5. **(Original)** The optical transceiver module as recited in claim 1, further comprising at least one electronic component positioned on one of a front or rear surface of the transceiver substrate.

6. **(Original)** The optical transceiver module as recited in claim 5, wherein the at least one electronic component includes at least one of: a laser driver; a signal amplifier; a status indicator component; a thermo-electric cooler; a current bias regulator; a capacitor; and, a resistor.

7. **(Currently Amended)** An optical transceiver module comprising:

a transceiver substrate, the transceiver substrate having a front surface and a rear surface and a top edge and a bottom edge;

an electrical connector positioned proximate the bottom edge of the transceiver substrate;

a receive optical assembly positioned proximate the top edge of the transceiver substrate;

a transmit optical assembly positioned proximate the top edge of the transceiver substrate; and

a transceiver housing defining an interior within which the ~~transceiver substrate~~, receive optical assembly[[,]] and transmit optical assembly are at least partially disposed, the transceiver housing interior being partitioned into first and second optical ports by an interior wall of the transceiver housing such that a portion of the receive optical subassembly is positioned in the first optical port and a portion of the transmit optical subassembly is positioned in the second port, including an interior wall that is substantially parallel to respective axes defined by the transmit and receive optical subassemblies, and the interior wall at least partially defines both first and second optical ports; a bottom of the transceiver housing defining a pair of optical port slots such that each of the optical port slots ~~communicates with~~ opens into a corresponding optical port.

8. – 9. **(Cancelled)**

10. **(Previously Presented)** The optical transceiver module as recited in claim 7, wherein the optical transceiver module is configured to physically and electrically interface with a host bus adaptor.

11. **(Cancelled)**

12. **(Original)** The optical transceiver module as recited in claim 7, wherein at least one of the following is positioned on at least one of the front surface and the rear surface of the transceiver substrate: a laser driver; a signal amplifier; a status indicator component; a thermo-electric cooler; a current bias regulator; a capacitor; and a resistor.

13. **(Previously Presented)** An optical computer interface assembly comprising;
a printed circuit board having at least one connector configured to electrically interface with a computer system; and
an optical transceiver module positioned on the printed circuit board, the optical transceiver module comprising:
a transceiver substrate including electronic circuitry, the transceiver substrate having a front surface and a rear surface and a top edge and a bottom edge;
an electrical connector positioned proximate the bottom edge of the transceiver substrate;
a receive optical sub-assembly positioned proximate the top edge of the transceiver substrate;
a transmit optical sub-assembly positioned to the right of the receive optical sub-assembly and proximate the top edge of the transceiver substrate, the transmit optical sub-assembly and receive optical sub-assembly being in electrical communication with the electronic circuitry of the transceiver substrate; and
a substantially box-shaped transceiver housing in which the transceiver substrate, receive optical sub-assembly, and transmit optical sub-assembly are positioned, at least one optical port slot being defined in a bottom of the substantially box-shaped transceiver housing.

14. **(Previously Presented)** The optical computer interface assembly as recited in claim 13, wherein at least one of the following is positioned on at least one of the front surface and the rear surface of the transceiver substrate: a laser driver; a signal amplifier; a status indicator component; a thermoelectric cooler; a current bias regulator; a capacitor; and a resistor.

15. **(Cancelled)**

16. **(Previously Presented)** The optical computer interface assembly as recited in claim 13, wherein the at least one optical port slot is located proximate the bottom edge of the transceiver substrate.

17. **(Previously Presented)** A computer system comprising:
a host bus adapter; and
an optical transceiver module as recited in claim 3 electrically connected to the host bus adapter.

18. **(Previously Presented)** The computer system as recited in claim 17, further comprising a face plate disposed adjacent the optical transceiver module.

19. **(Previously Presented)** A PCI card comprising:
a host bus adapter; and
an optical transceiver module as recited in claim 3 electrically connected to the host bus adapter.

20. **(Previously Presented)** A PCMCIA card comprising:
a host bus adapter; and
an optical transceiver module as recited in claim 3 electrically connected to the host bus adapter.

21. **(Previously Presented)** A PCI card comprising:
a host bus adapter; and
an optical transceiver module as recited in claim 10 electrically connected to the host bus adapter.

22. **(Previously Presented)** A PCMCIA card comprising:
a host bus adapter; and
an optical transceiver module as recited in claim 10 electrically connected to the host bus adapter.